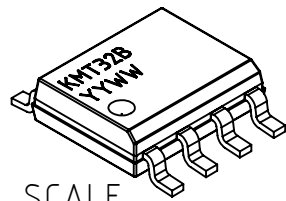
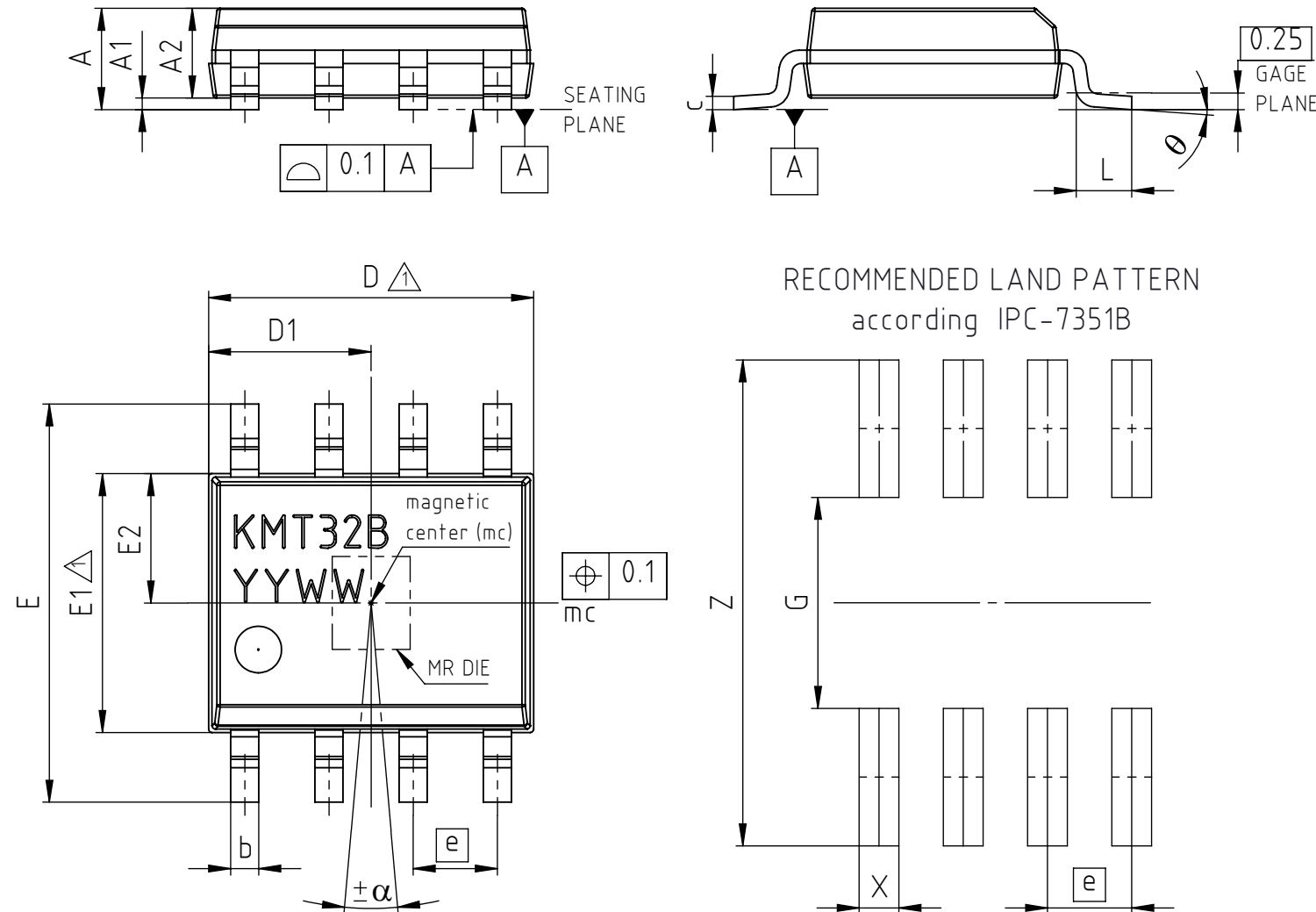


REV	DESCRIPTION	DATE	DWN	APVD
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SCALE
5:1



SYMBOL	DIMENSIONS		
	MIN.	NOM.	MAX.
A	1.35	-	1.75
A1	0.10	-	0.25
A2	1.25	-	1.50
b	0.35	-	0.50
c	0.17	-	0.24
D	4.80	-	5.00
D1	-	2.45	-
E	5.80	-	6.20
E1	3.80	-	4.00
E2	-	1.95	-
e	-	1.27	-
G	3.18	-	-
L	0.40	-	1.27
X	-	-	0.60
Z	-	-	7.32
θ	0°	-	8°
α	-5°	0°	+5°

NOTE:

1. DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

2. THIS PART IS COMPLIANT WITH JEDEC MS-012

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN Schreiner	12/15/2022	TE Connectivity	
		CHK Stute	05/03/2024		
DIMENSIONS: mm		APVD Pieper	05/06/2024	NAME C0 KMT32B ANGLE ANALOG 8L SOIC	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 1 PLC ± 2 PLC ± 3 PLC ± 4 PLC ± ANGLES ±°		PRODUCT SPEC	—		
		APPLICATION SPEC	—		
MATERIAL —		WEIGHT —	—	SIZE A3	CAGE CODE —
		FINISH —	—	DRAWING NO G-MRCD-015	RESTRICTED TO —
		CUSTOMER DRAWING		SCALE 10:1	SHEET 1 OF 1
				REV A	